PATENT APPLICATION Docket No: 14993.13

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of)	
	Joseph Walsh et al.)	
Serial No.:	Not Yet Assigned)	
Filed:	July 7, 2003)	
For:	DUAL DIFFERENTIAL-INPUT AMPLIFIER)	

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Please find, pursuant to 37 C.F.R. § 1.98(a)(1), the enclosed Form PTO-1449 which contains a list of all patents, publications, or other items that have come to the attention of one or more of the individuals designated in 37 C.F.R. § 1.56(c). While no representation is made that any of these references may be "prior art" within the meaning of that term under 35 U.S.C. §§ 102 or 103, the enclosed list of references is disclosed so as to fully comply with the duty of disclosure set forth in 37 C.F.R. § 1.56.

Moreover, while no representation is made that a specific search of office files or patent office records has been conducted or that no better art exists, the undersigned attorney of record believes that the enclosed art is the closest to the claimed invention (taken in its entirety) of which

the undersigned is presently aware, and no art which is closer to the claimed invention (taken in its entirety) has been knowingly withheld.

In accordance with 37 C.F.R. §§ 1.97 and 1.98, a copy of each of the listed references or relevant portion thereof is also enclosed.

Since all listed references are either in the English language or are accompanied by a translation into English, no concise explanation of relevance is required under 37 C.F.R. § 1.98(a)(3).

DATED July 7, 2003.

Respectfully submitted,

ADRIAN J. LEE

Attorney for Applicant Registration No. 42,785

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PATENT TRADEMARK OFFICE

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				Docket Number (Optional)		Application Number					
INFORMATION DISCLOSURE CITATION					14993.13 Applicant(s)		Not Yet Assigned				
	II4r O	(Use several sheets if necessal			Joseph Walsh et al.						
					Filing Date		Group Art Unit				
NO. N					July 7, 2003 Not Yet Assigned						
		T		U.S. PA 1	ENT DOCUMENTS	,		Υ			
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	<u> </u>	NAME CLASS SUBCLASS		FILING DATE IF APPROPRIATE				
	1	6,181,169	1/30/2001	Hu		327	77	10/28/1999			
	2	5,789,949	8/4/1998	Giordano et al.		327	63	9/25/1996			
	3	4,859,961	8/22/1989	Davenport		330	253	12/19/1998			
	4	4,859,872	8/22/1989	Hyakutake		307	269	3/23/1988			
	5	4,825,100	4/25/1989	Caspell		307	279	4/4/1988			
	6	4,585,961	4/29/1986	Daubert		307	491	1/19/1984			
	7	5,142,244	8/25/1992	Glica et	Glica et al.		253	5/6/1991			
	8	6,124,759	9/26/2000	Migliavacca		330	253	4/9/1998			
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				FOREIG	N PATENT DOCUMENTS						
	REF	DOCUMENT NUMBER DATE			COUNTRY		SUBCLASS	Translation NO			
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		ICMOS Apolog Circuit I	Perion: Phillip F			luthor, Title, I	Date, Pertinent Pa	ges, Etc.)			
	CMOS Analog Circuit Design; Phillip E. Allen & Douglas R. Holberg 1987 Copyright Holt, Rinehart and Winston p 356 & 410										
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

EXAMINER

DATE CONSIDERED